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8-203

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kiochiro TANAKA
Serial No. : 09/842,797
Filed : April 27, 2001
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Art Unit : 2822
Examiner : Maria F. Guerrero

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR TWO-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated February 12, 2003, be extended for two months to and including July 14, 2003 (July 12th and July 13th, 2003 were a Saturday and Sunday).

Enclosed is a check for \$410 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7/14/03


John F. Hayden
Reg. No. 37,640

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